

FIG. 2

2/9

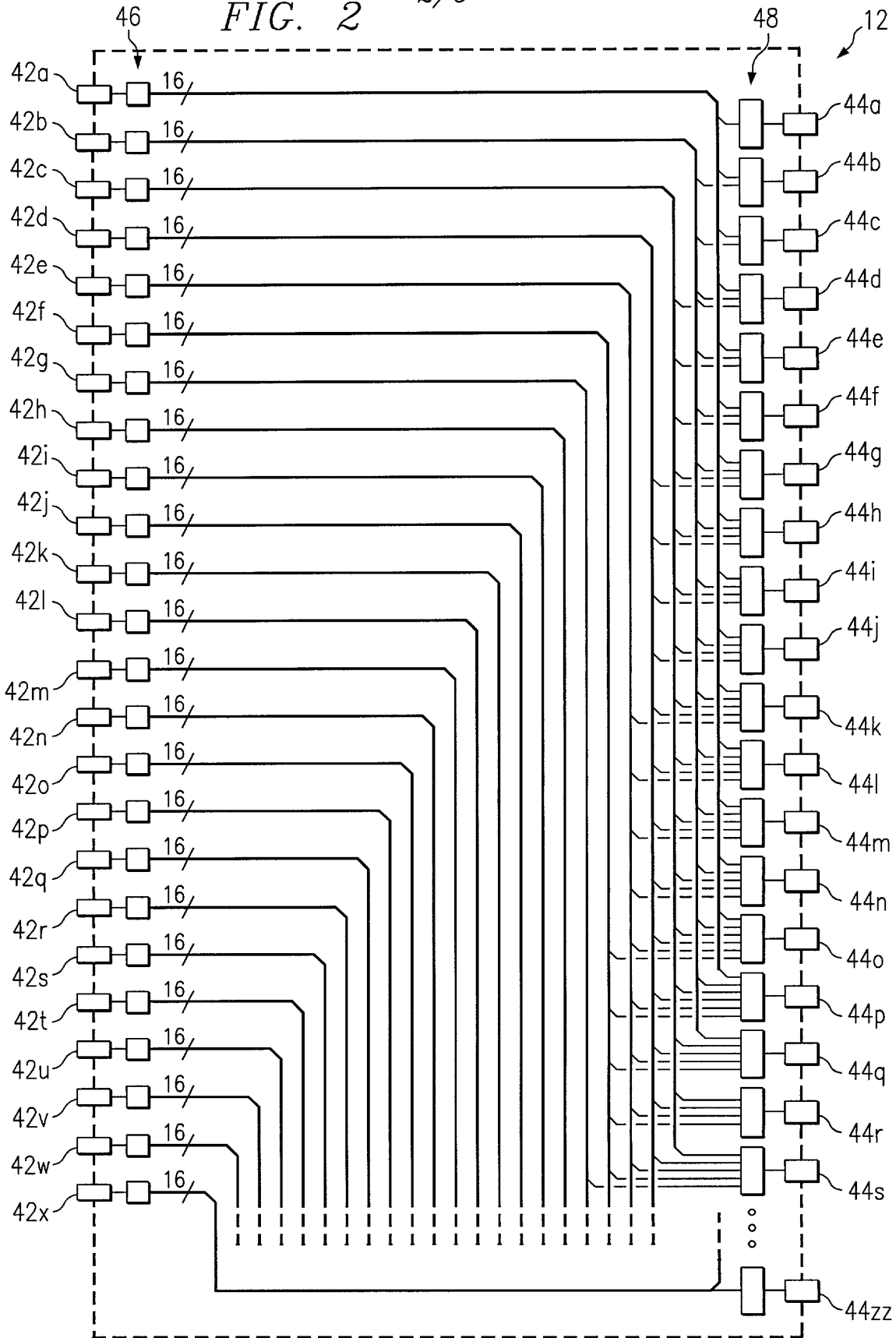


FIG. 3A

3/9

INPUT PORT	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	2	2	2	2
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	7	8	9	0	1	2	3
201	x																						
202	x	x																					
203	x	x																					
204	x	x	x																				
205	x	x	x																				
206	x	x	x																				
207	x	x	x	x																			
208	x	x	x	x																			
209	x	x	x	x																			
210	x	x	x	x																			
211	x	x	x	x	x																		
212	x	x	x	x	x																		
213	x	x	x	x	x																		
214	x	x	x	x	x																		
215	x	x	x	x	x	x																	
216	x	x	x	x	x	x																	
217		x	x	x	x	x																	
218			x	x	x	x																	
219			x	x	x	x	x																
220				x	x	x	x																
221				x	x	x	x																
222				x	x	x	x																
223					x	x	x	x															
224					x	x	x	x															
225					x	x	x	x															
226					x	x	x	x															
227						x	x	x	x														
228						x	x	x	x														
229						x	x	x	x														
230						x	x	x	x														
231							x	x	x	x													
232							x	x	x	x													
233							x	x	x	x													
234							x	x	x	x													

TO FIG. 3B

OUTPUT PORT

TO FIG. 3C

FROM FIG. 3B

[illegible]

6/9

323 324

TO FIG. 4B

7/9

FIG. 4B

FROM FIG. 4A

OUTPUT PORT	435							X	X	X																				S	C
	436							X	X	X																				S	C
	437							X	X	X	X																			S	C
	438							X	X	X	X																			S	C
	439								X	X	X																			S	C
	440								X	X	X																			S	C
	441								X	X	X																			S	C
	442								X	X	X	X																		S	C
	443								X	X	X	X																		S	C
	444									X	X	X																		S	C
	445									X	X	X																		S	C
	446									X	X	X																		S	C
	447									X	X	X	X																	S	C
	448									X	X	X	X																	S	C
	449										X	X	X																	S	C
	450										X	X	X																	S	C
	451										X	X	X																	S	C
	452										X	X	X	X																S	C
	453										X	X	X	X																S	C
	454											X	X	X																S	C
	455											X	X	X																S	C
	456											X	X	X	X															S	C
	457											X	X	X	X															S	C
	458												X	X	X															S	C
	459												X	X	X															S	C
	460												X	X	X															S	C
	461												X	X	X	X														S	C
	462												X	X	X	X														S	C
	463													X	X	X														S	C
	464													X	X	X														S	C
	465													X	X	X														S	C
	466													X	X	X	X													S	C
	467													X	X	X	X													S	C
	468														X	X	X													S	C
	469														X	X	X													S	C

TO FIG. 4C

FROM FIG. 4B

[illegible]

FIG. 5 508

INPUT PORT	5	5	5	5	5	5	5	SWEEPER
	0	0	0	0	0	0	0	
	1	2	3	4	5	6	7	
601	x	x	x	x	x	x	x	s
602	x	x	x	x	x	x	x	s
603	x	x	x	x	x	x	x	s
604	x	x	x	x	x	x	x	s
605	x	x	x	x	x	x	x	s
606	x	x	x	x	x	x	x	s
607	x	x	x	x	x	x	x	s
608	x	x	x	x	x	x	x	s
609	x	x	x	x	x	x	x	s
610	x	x	x	x	x	x	x	s
611	x	x	x	x	x	x	x	s
612	x	x	x	x	x	x	x	s
613	x	x	x	x	x	x	x	s
614	x	x	x	x	x	x	x	s
615	x	x	x	x	x	x	x	s
616	x	x	x	x	x	x	x	s
617	x	x	x	x	x	x	x	s
618	x	x	x	x	x	x	x	s
619	x	x	x	x	x	x	x	s
620	x	x	x	x	x	x	x	s
621	x	x	x	x	x	x	x	s
622	x	x	x	x	x	x	x	s
623	x	x	x	x	x	x	x	s
624	x	x	x	x	x	x	x	s